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ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
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2824

**CONTINUING DATA VERIFIED:
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NONE

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** FOREIGN APPLICATIONS VERIFIED:
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NONE

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Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials <i>J. H. H. 9/14</i>		ATTORNEY DOCKET NO NSC1-G9800 [P05051]
TITLE : Integrated circuit and method for testing same using single pin to control test mode and normal mode operation U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims <input type="checkbox"/> Print Claim for O.G. <input type="checkbox"/>
ISSUE FEE		Primary Examiner	DRAWING Sheets Drwg. <input type="checkbox"/> Figs. Drwg. <input type="checkbox"/> Print Fig. <input type="checkbox"/>
Amount Due	Date Paid	PREPARED FOR ISSUE	Application Examiner
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